



Chunghwa Picture Tubes, Ltd.

Product Specification

To : RB Electronics

Date : 080506

TFT LCD

CLAA089NA0ACW

ACCEPTED BY : (V0.8)

Tentative

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REVISION STATUS

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1. OVERVIEW

CLAA089NA0ACW is 8.9" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel, driver ICs, control circuit and LED backlight.

The 8.9" screen produces 1024× RGB X600 resolution image. By applying 6 bits digital data, 262K color images are displayed. The LCD is driven by a single input voltage (3.3 V).

General specifications are summarized in the following table:

ITEM	SECIFICATION
Display Area (mm)	195.072 (H) × 114.3 (V)
Number of Pixels(dot)	1024(H) × 3(RGB)× 600(V)
Pixel Pitch (mm)	0.1905 (H) × 0.1905 (V)
Color Pixel Arrangement	RGB vertical stripe
Display Mode	Normally white, TN
Number of Color	262,144
Viewing Direction	6 o'clock
Response Time (Tr+Tf)	20 ms (typ.)
Brightness(cd/m ²)	220 nit (typ.)
Viewing Angle (CR ≥ 10)	140 degree (Horizontal) 120 degree (Vertical)
Electrical Interface	LVDS
Power Consumption(W)	3.0 W (typ.)
Outline Dimension(mm)	210(W) × 127.8(H) × 5.42(D)
Module Weight(g)	195 (typ.)
BL unit	LED
Surface Treatment	Anti-Glare, Surface hardness: 3H

2. ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT	NOTE
Power Supply Voltage for LCD	V _{cc}	-0.3	4.0	V	
Signal Input Voltage	RxIN0+ ~ RxIN2+ RxIN0- ~ RxIN2- Rx CLK IN +/-	-0.3	V _{cc} + 0.3	V	
Static Electricity	VESDc	-200	+200	V	*2)
	VESDm	-15K	+15K	V	
ICC Rush Current	IRUSH	-	1	A	*3)
Operation Temperature	T _{op}	-20	70	°C	*1)
Storage Temperature	T _{stg}	-30	80	°C	*1)

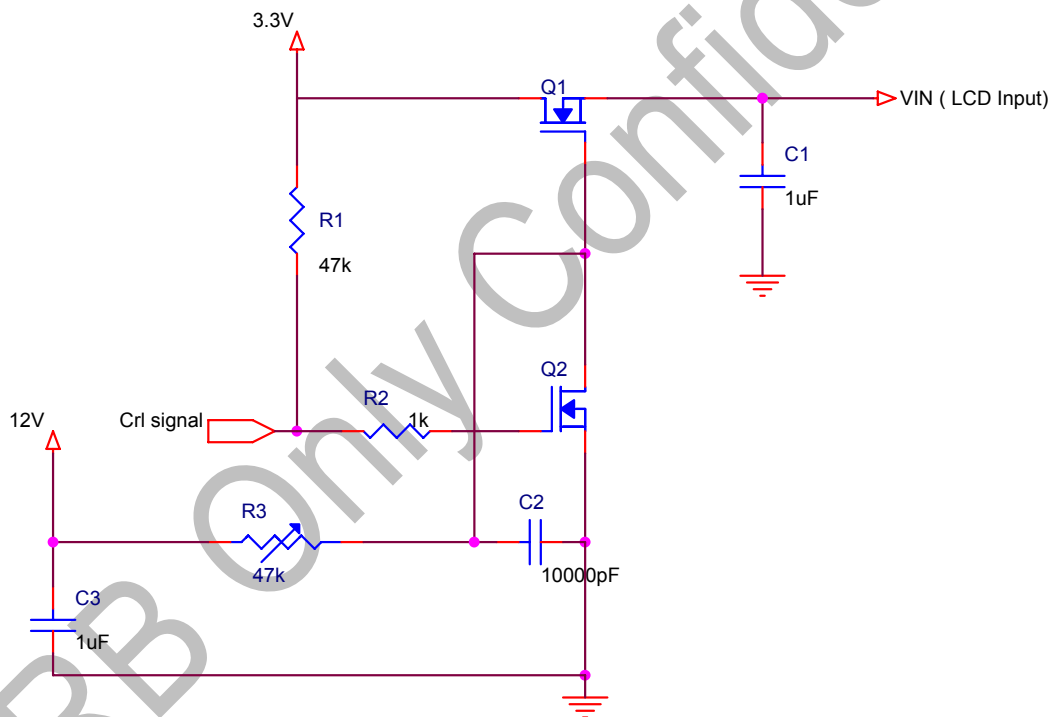
*1) If users use the product out of the environmental operation range (temperature and humidity), it will have visual quality concerns.

*2) Test Condition: IEC 61000-4-2

VESDc : Contact discharge to input connector

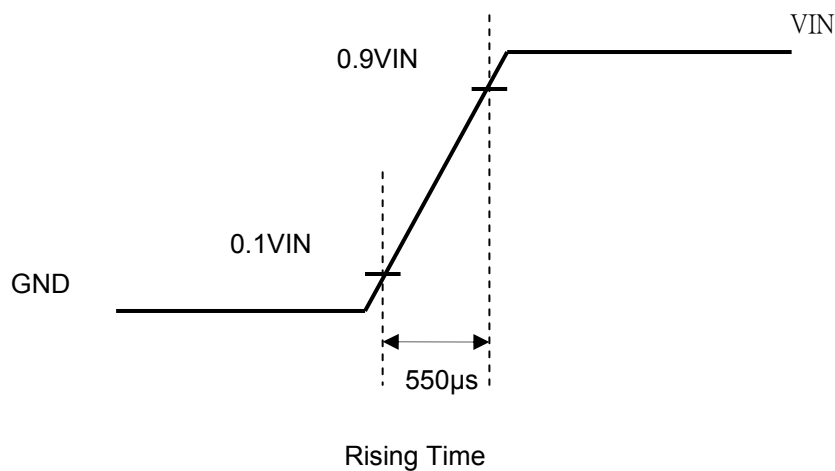
VESDm : Discontact discharge to module

*3) The input pulse-current measurement system is as below:



Control signal: High (+3.3V) → Low (GND)

Supply Voltage of rising time should be from R3 and C2 tune to 550 μs.



3. ELECTRICAL CHARACTERISTICS

3.1 TFT-LCD Power Supply Voltage

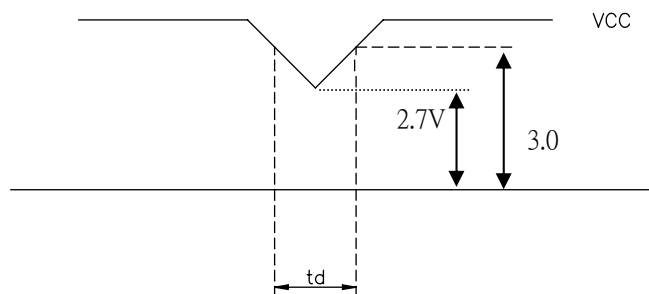
(Ta=25°C)

ITEM	SYMBOL	MIN.	Typ.	MAX.	UNIT	NOTE
Power Supply Voltage For LCD	VCC	3.0	3.3	3.6	V	*1)
Power Supply Voltage For LED	VDD	4.5	5	5.5	V	
Logic Input Voltage	VIN	0	-	2.4	V	*2)
	VCM	0.3	1.2	2.1	V	*2)
	VID	0.1	-	0.6	V	*2)
	VTH	-	-	100	mV	*2)
	VTL	-100	-	-	mV	*2)
ADJ Input Voltage	VIH	3.0		3.3	V	
	VIL	GND		0.3	V	

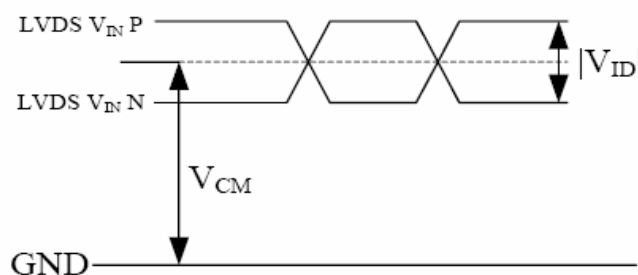
*1) VCC-dip condition:

$2.7\text{ V} \leq \text{VCC} < 3.0\text{V}$, $t_d \leq 10\text{ ms}$

$\text{VCC} > 3.0\text{V}$, VCC-dip condition should be the same as VCC-turn-on condition.



*2) LVDS signal



Note:

VCM: Common Mode Voltage Offset

| VID | : Differential Input Voltage

VTH: Differential Input High Threshold Voltage

VTL: Differential Input Low Threshold Voltage

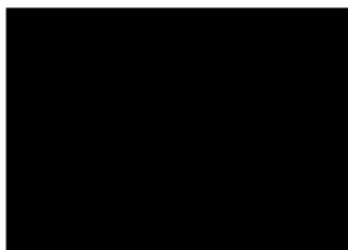
3.2 TFT-LCD Current consumption

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
LCD Power Current	ICC	--	260	350	mA	*1)
LED Power Current	IDD	--	420	480	mA	*2)

*1) Typical: Under 64 gray pattern
Maximum: Under black pattern



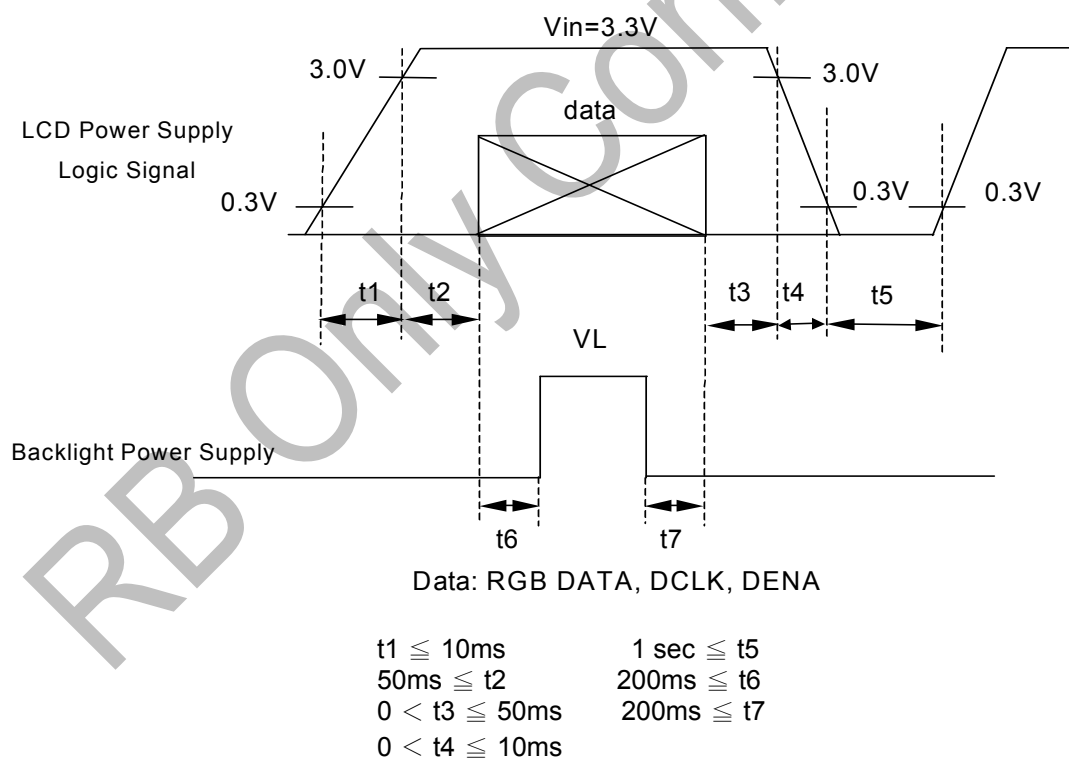
(a) 64 Gray Pattern



(b) Black Pattern

*2) Typical: VDD = 5V
Maximum: VDD = 4.5V

3.3 Power and Signal sequence



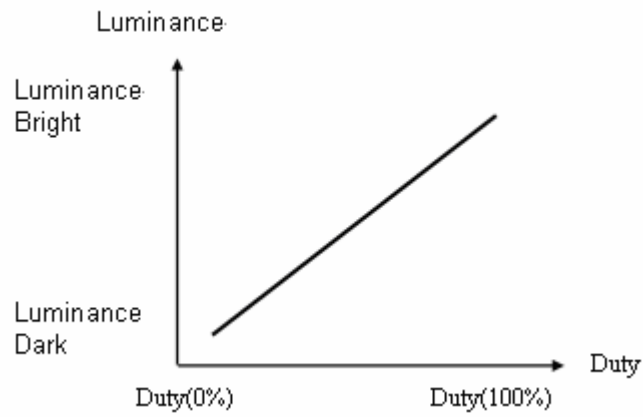
4. INTERFACE CONNECTION:

4.1 Pin Assignment

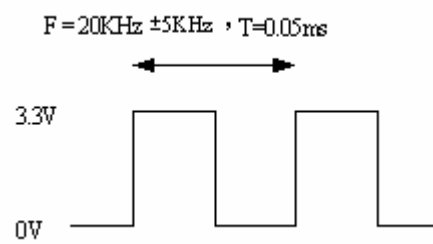
Pin NO.	Symbol	DESCRIPTION
1	AVSS	Power Ground
2	VCC	Power Supply for Digital circuit
3	VCC	Power Supply for Digital circuit
4	VEDID	3.3V
5	ADJ	Brightness control for LED B/L
6	CLKEDID	EDID Clock Signal
7	DATAEDID	EDID Data Signal
8	RXIN0-	Negative LVDS differential data inputs
9	RXIN0+	Positive LVDS differential data inputs
10	AVSS	Power Ground
11	RXIN1-	Negative LVDS differential data inputs
12	RXIN1+	Positive LVDS differential data inputs
13	AVSS	Power Ground
14	RXIN2-	Negative LVDS differential data inputs
15	RXIN2+	Positive LVDS differential data inputs
16	AVSS	Power Ground
17	RXCLK-	Negative LVDS differential clock inputs
18	RXCLK+	Positive LVDS differential clock inputs
19	AVSS	Power Ground
20	NC	NC
21	NC	NC
22	AVSS	Power Ground
23	AVSS	Power Ground
24	VLED	Power Supply for LED(VLED=5.0±0.5)
25	VLED	Power Supply for LED(VLED=5.0±0.5)
26	VLED	Power Supply for LED(VLED=5.0±0.5)
27	NC	NC
28	NC	NC
29	NC	NC
30	NC	NC

[Note]:

- 1) To reserve NC pin, don't make it connect with GND or any other signal.
- 2) AVSS pin must be connected with GND. Don't let it be an empty pin.
- 3) Adjust control pin (ADJ) controls brightness. The bigger pulse duty, the brighter luminance.



4) ADJ signal=0~3.3V, operation frequency: 20KHz \pm 5KHz



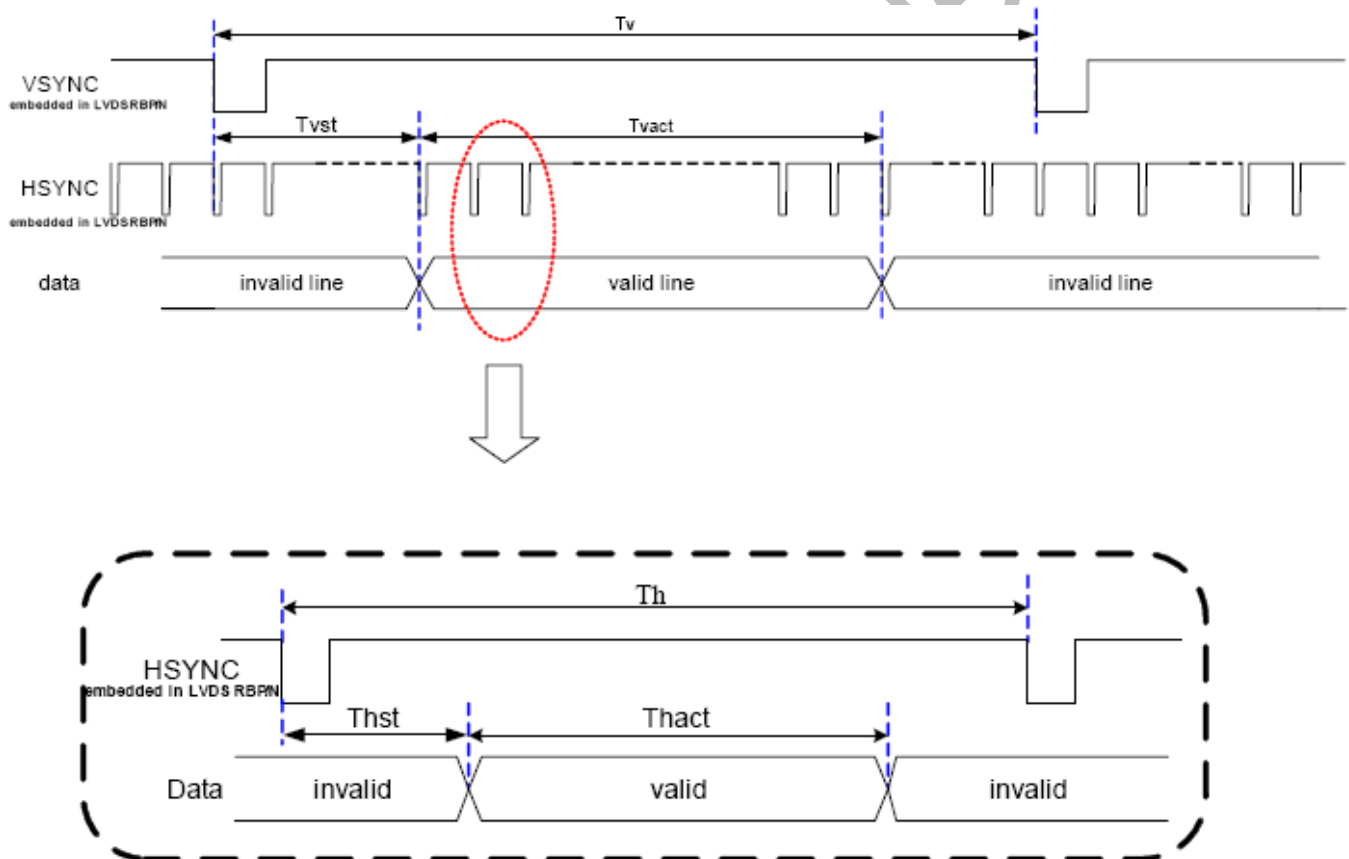
5. INPUT SIGNAL

5.1 Timing Specification

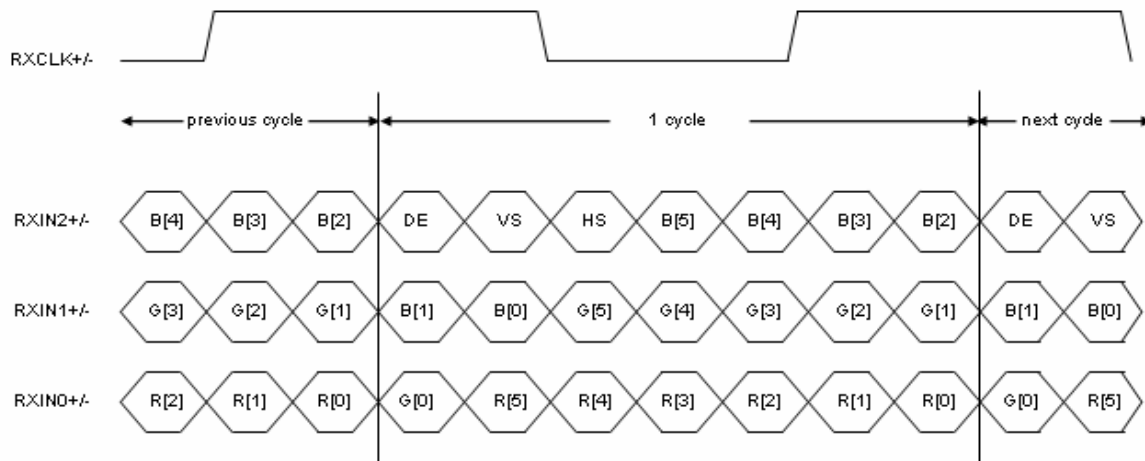
Item			SYMBOL	MIN.	TYP.	MAX.	UNIT
LVDS	CLK frequency		fCLKin	39.05	45	51.42	MHz
	CLK period		tCLKin	25.61	22.22	19.45	ns
LCD Input timing	DENA	Horizontal	Horizontal Period	t _H	1160	1200	tCLK
			Horizontal Valid	t _{HA}	1024	1024	tCLK
			Horizontal Blank	t _{HB}	136	176	tCLK
		Vertical	Frame	fV	55	60	Hz
			Vertical Period	t _V	612	625	t _H
			Vertical Valid	t _{VA}	600	600	t _H
			Vertical Blank	t _{VB}	12	25	t _H

5.2 Timing Chart

(1) Input Timing Diagram



(2) LVDS Input Data Mapping



Note: R/G/B[7] are MSB and R/G/B[0] are LSB.

5.3 Color Data Assignment

COLOR	INPUT	R DATA						G DATA						B DATA					
	DATA	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
		MSB					LSB	MSB					LSB	MSB					LSB
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
BASIC	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
COLOR	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	CYAN	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
GREEN																			
	GREEN(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
BLUE																			
	BLUE(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

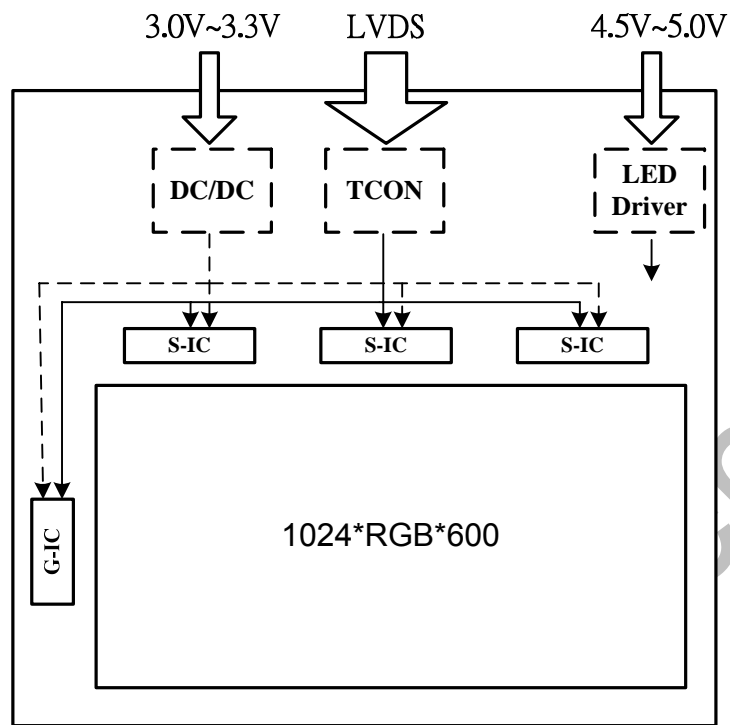
[Note]:

1) Definition of Gray level:

Color(n): n shows the gray level. n is the higher and the light brighter.

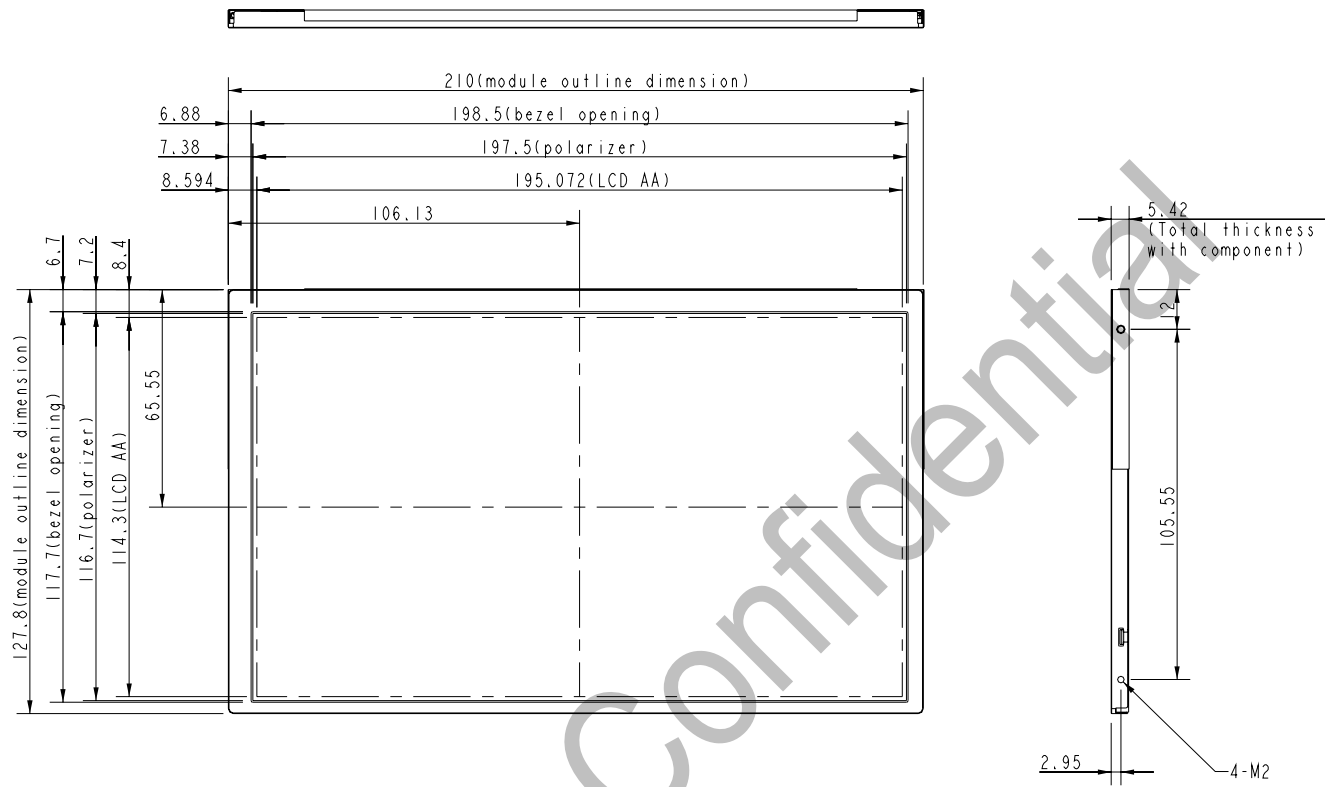
2) Data: 1-High, 0-Low.

6. BLOCK DIAGRAM

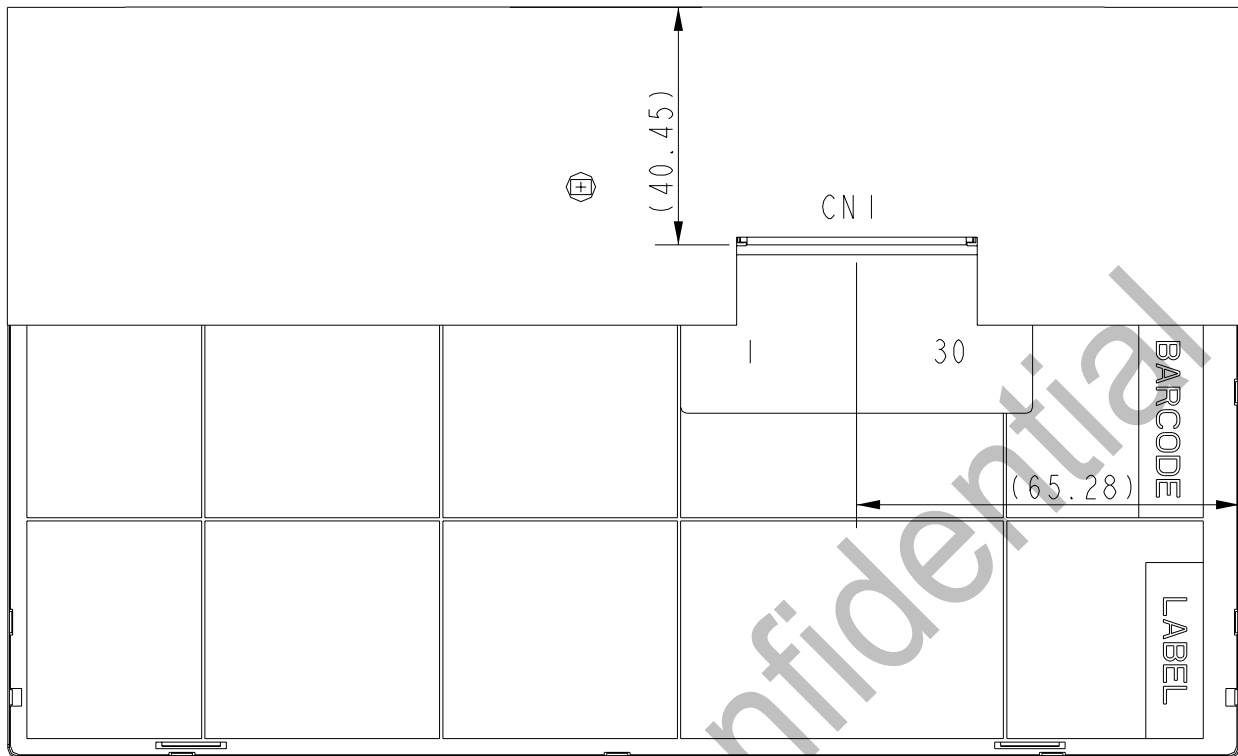


7. MECHANICAL DIMENSION

7.1 Front Side



7.2 Rear Side



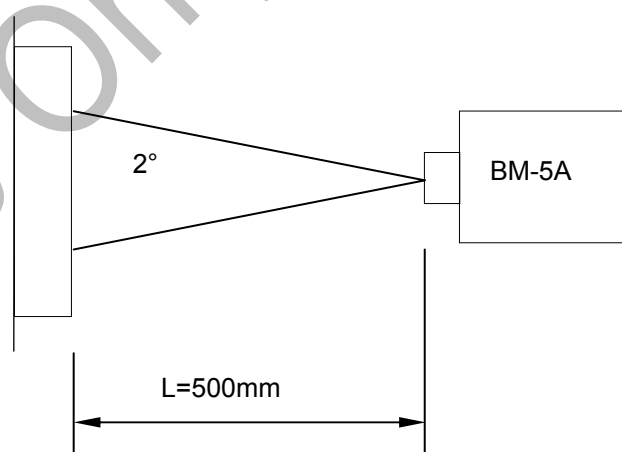
- [Note]:
1. Tolerance is $\pm 0.3\text{mm}$ unless noted
 2. CN1: STARCONN 093F30
 3. SIDE MOUNT M2 SCREW TORQUE Max. $2.5\text{ kgf}\cdot\text{cm}$
 4. SIDE MOUNT M2 SCREW Length Max. 2.0 mm

8. OPTICAL CHARACTERISTICS

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	Remarks
Contrast Ratio		CR	Point-5	300	400	-	-	*1)*2)*3)
Luminance		Lw	Point-5	180	220		cd/m ²	*1)*3)
Luminance Uniformity		ΔL		70	80	-	%	*1)*3)
Response Time (White - Black)		Tr+ Tf	Point-5	-	20	35	ms	*1)*3)*5)
Viewing Angle	Horizontal	ψ	CR ≥ 10 Point-5	120	140	-	°	*1)*2)*4)
	Vertical	θ		100	120	-	°	*1)*2)*4)
Color Coordinate	White	W _x W _y	Point-5	0.273 0.289	0.313 0.329	0.353 0.369	-	*1)*3)
	Red	R _x R _y		0.554 0.297	0.594 0.337	0.634 0.377		
	Green	G _x G _y		0.299 0.548	0.339 0.588	0.379 0.628		
	Blue	B _x B _y		0.108 0.055	0.148 0.095	0.188 0.135		

[Note]:

*1) Measurement condition: 25°C ± 2°C, 60% ± 10% RH, under 10 Lux in the dark room. Measuring with BM-5A (TOPCON) under viewing angle 2°, V_{CC} = 3.3V, V_{adj} = 3.3V, Duty 100%, after lighting 10 mins.



*2) Definition of contrast ratio:

Contrast Ratio (CR) = (White) Luminance of ON / (Black) Luminance of OFF

*3) Definition of Luminance: Measuring white luminance on the point 5 as figure 8-1

Definition of Luminance Uniformity: Measuring white luminance on the point1 to 9 as figure 8-1

$$\Delta L = [L(\text{Min})/L(\text{Max})] \times 100\%$$

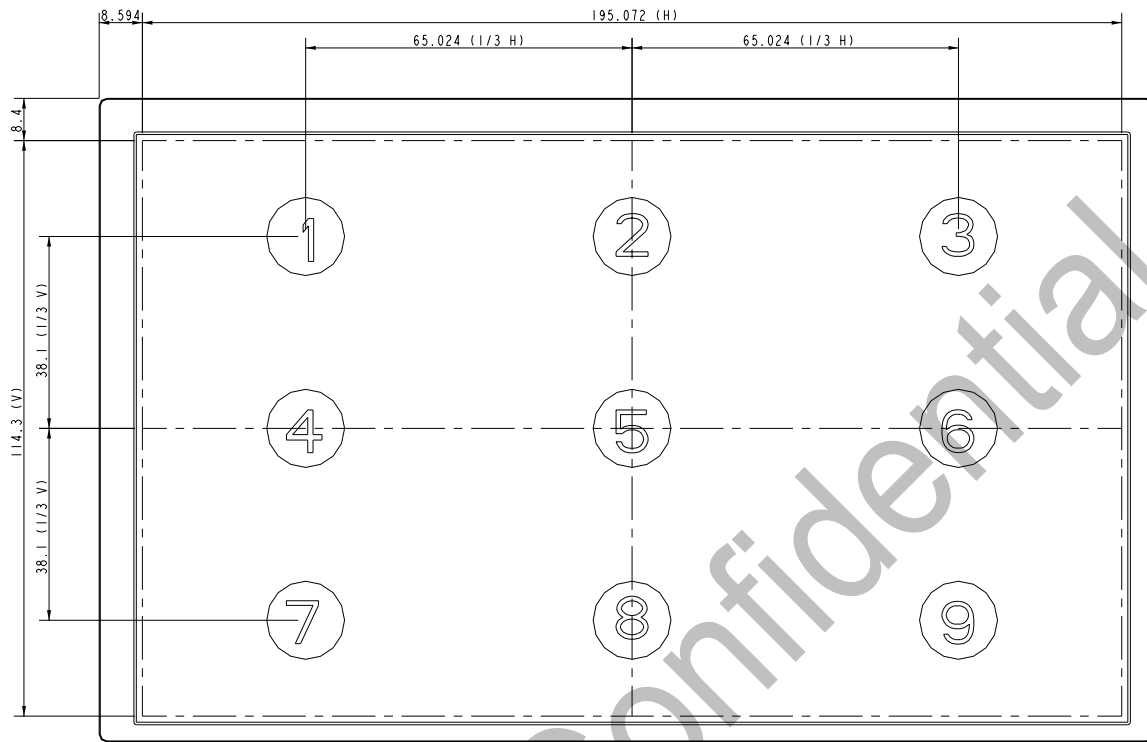


Fig. 8-1 Measuring point

*4) Definition of Viewing Angle(θ, ψ):

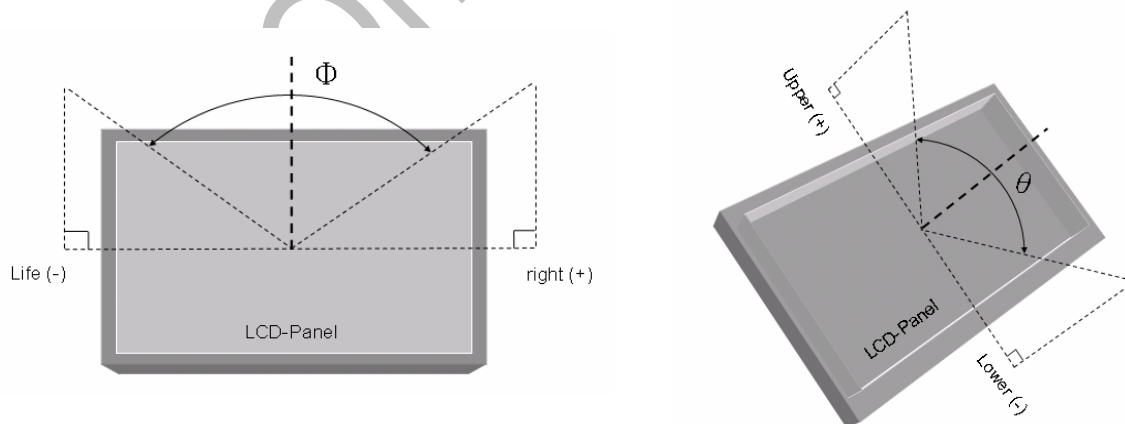


Fig. 8-2 Definition of Viewing Angle

*5) Definition of Response Time

The response time is defined as the time interval between the 10% and 90% amplitudes. Refer to the figure 8-3 as below.

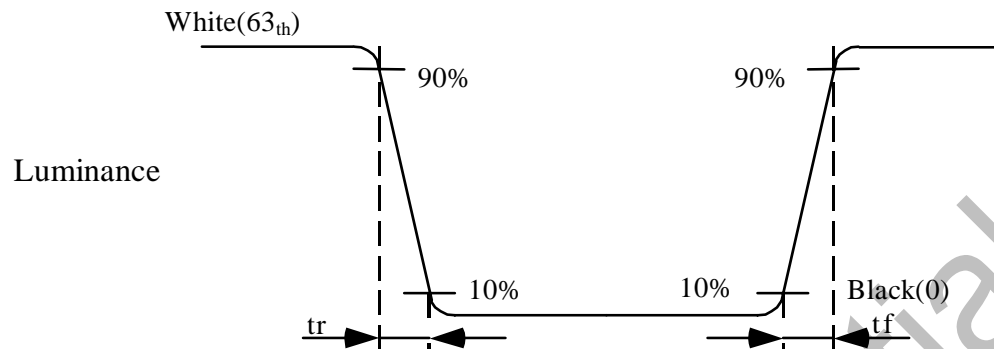


Fig. 8-3 Definition of Response Time

9. RELIABILITY TEST CONDITIONS

9.1 Temperature and Humidity

TEST ITEMS	CONDITIONS
High Temperature Operation	70° C ; 240 Hrs
High Temperature Storage	80° C ; 240 Hrs
High Temperature High Humidity Operation	60° C ; 90% RH ; 240 Hrs (No condensation)
Low Temperature Operation	-20° C ; 240Hrs
Low Temperature Storage	-30° C ; 240 Hrs
Thermal Shock	-30° C (0.5Hr)~80° C (0.5Hr) 200 CYCLE

9.2 Shock & Vibration

TEST ITEMS	CONDITIONS
SHOCK (NON-OPERATION)	<ul style="list-style-type: none"> ● Shock level: 980m/s²(equal to 100G). ● Waveform: half sinusoidal wave, 6ms. ● Number of shocks: one shock input in each direction of three mutually perpendicular axes for a total of three shock inputs.
VIBRATION (NON-OPERATION)	<ul style="list-style-type: none"> ● Frequency range: 8~33.3Hz ● Stroke : 1.3 mm ● Vibration: sinusoidal wave, perpendicular axis (both x,z axis: 2Hrs , and y axis: 4Hrs). ● Sweep: 2.9G, 33.3 Hz -400 Hz ● Cycle: 15 min

9.3 Judgment Standard

The judgment of the above test should be made as follow:

Pass: Normal display image with no obvious non-uniformity and no line defect.
Partial transformation of the module parts should be ignored.

Fail: No display image, obvious non-uniformity, or line defects.